



San Francisco Bay Area Council

Networking Opportunities at local Chapter Meetings, Tutorials, Workshops, PACE, Consultants Network Please Post and Circulate within your Company/Lab Conferences, Career Enhancement

July 15, 2005

Serving the San Francisco, Oakland-East Bay, and Santa Clara Valley Sections of the IEEE

For an online version of this Council announcement, with active links, visit www.e-grid.net/announcements/grid0507b.html

below: Semiconductor Mfgng, MEMS/NEMS Packaging Confs Magnetic Recording, BioInformatics Confs **University Engineering Classes On-Line**

DSP, Networking, Finance, Myers-Briggs Classes

Subscribe

Download the July GRID.pdf (850 kB) -- details on upcoming Chapter meetings, classes, conferences on a wide variety of topics. See our QuickRef Calendar for a more detailed listing of Chapter meetings.

Chapter Meetings for July, August:

OEB-Comm - 7/19 | IEEE 802.16 Wireless Metropolitan Area Networks - the interface standard for fixed wireless networks for broadband delivery ... [more]

SCV-Nano - 7/19 | Molecular-Scale Technologies for Electronics and Life Sciences - new ways to apply the techniques of electronics technology to the field of life sciences ... [more]

SF-Comm - 7/19 | Technology Policy Roundtable: Ultra Wide of semiconductor processing equipment open to external use ... [more] Band (UWB) - informal presentations and interesting industry discussions ... [more]

SCV-CPMT - 7/21 | Reliability of Lead-free Solder Joints: Intermetallic Reactions - solder reaction, spalling, and Kirkendall void formation ... [more]

SCV-CE - 7/26 | Wireless at Home: Home Infotainment Networking - impact of internet sharing, entertainment systems in the Conductivity of Conductive Plastics - ... [more] home ... [more]

SCV-IM - 8/3 | Contamination Analysis using Surface Analytical Tools - what is possible, with practical examples ...

 $\ensuremath{\mathsf{SJSU}}$ - $8/9 \mid \textbf{DSP System Design}$ and Wireless Transmitters -4-day classes with labs ... [more]

LSS - 8/12 | BioImage Data Mining, 3D Molecular Structures, BioSPICE and Controlling Complexity inexpensive 1-day workshops in bio-sciences, held at Stanford ...

SJSU - 8/16 | FPGA DSP System Design and High-Speed Data Networks - 4-day classes with labs ... [more]

SCV-IM - 9/7 | Introduction to the Stanford Nanofabrication Facility, with Research Examples - an NSF Center with a variety

SCV-CPMT - 9/14 | Kirkendall Voids in Lead-Free Solder Joints: A Reliability Issue - ... [more]

SCV-CPMT - 10/12 | Imprinted Circuits: A Different, Potentially More Cost-Effective Technology - ... [more]

SCV-CPMT - 11/9 | Carbon Nanotubes: Enhancing

Note to All IEEE Members

Professional Engineer Exam Review Course

The SF and OEB Sections, along with SF PACE, have organized a review course for those wishing to take the PE exam in October. The course provides a review of Electrical Engineering subjects for the test, and the course instructors will give presentations and lead students through problemsolving exercises. The 10 weekly sessions are on Thursday evenings, starting August 18th, and are held near the 19th street BART station in Oakland, for easy access. Cost is a low \$125 for members, and enrollment will be limited. Please download the course flyer, for more information and registration details. Let them know right away if you are NOT yet registered for the October 28th exam.

The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our Council Marketplace page. Thank you for supporting our advertisers.

ISSM'05 IEEE International Symposium on

Semiconductor Manufacturing [more]

- -- San Jose Fairmont Hotel
- -- Workshops: Monday, September 12
- -- Sessions: Tuesday Thursday, September 13-15

The Fourteenth ISSM is the largest world-wide forum specifically designed for semiconductor device manufacturers and suppliers. This year's event covers timely and important topics:

- Factory Design - Manufacturing Strategy and Structure - Ultra Clean Technology - Process and Metrology Equipment - Manufacturing Control and Execution - Process Materials Optimization Environment, Safety & Health - Robust Engineering - Advanced Backend Processing - Process Control and Monitoring - Yield Enhancement

Two half-day workshops: - Impact on Manufacturing of Introducing New Materials in IC Production -Design for Manufacturing and Advanced Process Control

Six keynote speakers from Tokyo Electron, NSC, UMC, TI, Sematech, Samsung

Earlybird registration discount through August 1st. See the one-page flyer in this month's GRID.pdf, or visit www.issm.com. Sponsors: IEEE Electron Devices and CPMT Societies, with SEMI and JSAP ASME Pacific Rim Technical Conference and Exhibition

Integration and Packaging of MEMS, NEMS, and Electronic Systems [more]

- San Francisco Westin St. Francis Hotel
- July 17-22 (Tutorials July 17, sessions/exhibits July 18-22)

Being held for the first time in the SF Bay Area, the InterPACK'05 Conference is an international forum for exchange of state-of-the-art knowledge in research, development, manufacturing, and application in the integration and packaging of MEMS, NEMS, and electronic systems. It covers the disciplines of microelectronics, photonics, microwave, MEMS and NEMS. Emerging knowledge, research results, new developments, and novel thermal, mechanical, electrical, and materials packaging product concepts are being presented.

Download the $\underline{one\text{-}page\ flyer}$ Review the program at www.asme.org/events/ IEEE Magnetics Society

16th Annual Magnetic Recording Conference: "Heads and Systems" [more]

- August 15-17
- Held at Stanford University in Palo Alto
- Join leading experts in the magnetic recording field

Main topics: Read heads, Write heads, Perpendicular recording heads and systems, Recording systems, Advanced coding/detection and Reliability/Mechanics.

The oral sessions will be held at the Hewlett Teaching Center Auditorium, and Stone Pine Plaza will be used for Posters and Bierstube. On-campus parking at no cost.

Download the <u>one-page flyer</u>

Get complete information at tmrc.nanointernational.org

Need consulting or outsourcing services? Check out the GRID Marketplace.

August 8-11 on the Stanford University campus (plenty of parking):

IEEE Computational Systems Bioinformatics Conference [more]

Come to CSB'05 for a broad spectrum of peer-reviewed papers covering the breadth and depth of the dynamically evolving field of bioinformatics. Our keynote speakers, paper and poster presentations, tutorials and social events have all been designed to cater to bioinformatics' eclectic mix of disciplines. The low registration fees make it possible for everyone to attend - it's especially easy for "locals" from the Bay Area.

What is "Bioinformatics"? - scientific and engineering disciplines bringing new biological discoveries to fields as varied as human health, agriculture, the environment, energy and biotechnology. Get started in engineering's newest field!

Tutorials on Monday: - Demonstration Projects in Clinical Informatics - Novel Visualization and Analysis Methods in Biolmaging - Computational Methods in MS-based Proteomics - Introduction to the Semantic Web for Bioinformatics - Pattern Discovery in Sequences and Structures - The Biological Data Deluge through Phylogenetics - Statistical Approaches to Analyzing Biological Networks - and more Associated Workshops on Friday, August 12 (only \$100) Need not register for CSB; only \$100 - Biolmage Data Mining and Informatics - Controlling Complexity - Exploring 3D Molecular Structures Using NCBI Tools - BioSPICE and Use Cases

See the one-page flyer in this month's $\underline{GRID.pdf}$, or visit $\underline{conferences.computer.org/bioinformatics}$.

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Polytechnic University's ePoly online graduate programs give you the chance to get ahead with your education and your career, without interfering with your busy schedule. You can pursue a Master's degree, a graduate certificate, or continue your education - all online. Apply online now for the Fall 2005 Semester starting September 1st: epoly.poly.edu/applynow

M.S. in Electrical Engineering (MSEE) [more]

Earning an M.S. degree online in Electrical Engineering will prepare you for an advanced level career as a practicing electrical engineer. Graduates of the MSEE program represent a well-respected group of highly motivated, practicing engineers. Many move on to conduct advanced research or pursue their PhDs.

M.S. in Telecommunication Networks (MSTN) [more]

Earning an M.S. degree online in Telecommunication Networks provides you the background that will cover all aspects of networking -- Internet banking, reservation systems, office information systems, and corporate networks.

Telecommunication Network Management Graduate Certificate [more]

A Graduate Certificate in Telecommunication Network Management may be pursued en route to the MSTN or MSEE - or simply to gain a specialization in this complex field of network

Wireless Communications Graduate Certificate [more]

Earning a graduate certificate in Wireless Communications will enable you to advance your career or begin an entirely new one. With a background in EE, the online courses in this program will provide you with specific knowledge required to work in this expanding market. These courses can count towards earning a Master's degree in Electrical Engineering.

Two college-level teaching positions available

Santa Clara University has an opening for a lecturer in EE (with emphasis in communications), and Cogswell College has an opening for an endowed chair in electrical engineering.

For more details: www.e-grid.net/docs/0507-positions.pdf

SJSU Department of Electrical Engineering:

August Short Courses 4 days, with labs On the SJSU campus (easy parking)

Two classes on August 9-12:

DSP System Design and Implementation [more]

Wireless Transmitters [more]

Three classes on August 16-19:

 ${\bf Embedded\ Systems\ and\ Embedded\ FPGAs\ [\underline{more}]}$

FPGA DSP System Design [more]

High-Speed Data Networks [more]

For full class outlines, prerequisites, fees, and lecturer biographies, see the Course Flyer: www.e-grid.net/docs/sjsu0508.pdf

Professional Skills Courses in the SF Bay Area

Choose from an array of skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters. Classes in July:

Finance for Non-Finance Professionals [more]

- July 21 at Exar Corp, Fremont

Getting Things Done Across Organizational Borders [more]

- July 21 at BEA Systems, San Jose

Managing Time & Multiple Priorities [more]
- July 22 at Sybase, Dublin

Communication & Conflict Management Using MBTI (Myers-Briggs) [more]

- August 17 at LSI Logic, Milpitas

Consultancy Partner Wanted

An electrical and mechanical engineering firm in Oregon with 35 years of experience is looking for an interested party to take over the company or join with the ultimate aim of taking ownership. Consulting is in the utilities, energy, forest, power and transmission industries in the Pacific Northwest and Alaska.

For more details: www.e-grid.net/docs/0507-consult.pdf

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Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See http://www.e-grid.net/

Correspondence to Paul Wesling, Council Communications Director