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May 18, 2004

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May-June Chapter Meetings - summary Conferences in the SF Bay Area

Professional Dev't Short Courses

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## Council/Section/Chapter Meetings and Events from mid-May onward

Your Career -- Your Future! Attend an evening Chapter meeting, to network with other professionals. The meetings/events below are listed in order by date. (You may use "Cntl-F" to search for a Chapter abbreviation or keyword.)

SCV-Mag - 5/18: "Dynamics, Damping and Defects in Thin Ferromagnetic Films" -

Disk drives can write bits every two nanoseconds, a time scale similar to the magnetic damping time, and damping will limit data rates in MRAM ... [more]

SCV-CNSV - 5/18: "Networking, Networking, Networking: the IEEE CNSV's Way" (Panel) - Sharing what we can do to be successful in this new world in which consultants find themselves ... [more]

SCV-EMB - 5/19: "Optical Biopsy in Gastroenterology" - Small bowel imaging, limitations of standard endoscopic and radiographic techniques, and the GIVEN capsule technology ... [more]

OEB-Comm - 5/20: "DSP for Packet Voice Telephony" - Transition of the telephone network from circuit-switched to packet-switched and VoIP: digital signal processing (DSP) requirements at various points of the network ... [more]

SCV-PES/IAS - 5/20: "Aluminum Cable in Commercial and Industrial Applications" - use of aluminum cable - history, industry codes, termination standards and recommended practices ... [more]

SCV PSE - 5/25: "Reformed Methanol Micro Fuel Cell System for Portable Power Applications" - Fuel cell systems for portable applications have higher power density than batteries ... [more]

SCV-EMS - 5/26: "Effective Virtual Meetings" and "Sustainable Systems for Off-the-Grid Communities" - Common pitfalls that geographically dispersed teams experience ... The vision of sustainable homes and communities — a dream within our reach ... [more]

SCV-REL - 5/26: "Cisco's High Level Failure Analysis Process" - The logistics, resources, and techniques used to provide failure information and initiate corrective action ... [more]

SCV-LEOS - 6/1: "Quantum Cryptography Using Autocompensating Fiber-Optic Interferometers" - IBM work on quantum cryptography systems and on low-noise single-photon detectors ... [more]

SCV-EDS - 6/8: "Reliability of Low-k Dielectrics: The Importance of the Barrier Interface" - reliability is determined by the barrier/dielectric interface - Post Etch Burn Out (PEBO) is an enabling sealing concept ... [more]

SCV-Comm - 6/9: "Cyberspace Security Issues and Challenges" - the Internet poses a unique set of security issues due to its openness and ubiquity ... [more]

SCV-PES/IAS - 6/11: **Summer Banquet** - Sumptuous dining high above Silicon Valley ... [more]

SCV-SP - 6/14: "Fortran 95, or Matlab meets C++" - surprising modern language features in Fortran 90 & 95 ... [more]

SCV-CNSV - 6/15: Consultants' Network Annual Dinner: "Positive Psychology" - Come for networking, dinner, and an entertaining speaker on psychology, success and self-help ... [more]

SCV-SSC - 6/17: "MicroPower Precision Floating Gate Voltage Reference" - A new low-power voltage reference design with high accuracy is targeted at portable systems ... [more]

SCV-Mag - 6/15: "It's a Small World, But I Wouldn't Want to Paint It" - the shift to small-form-factor disk drives ... [more]

OEB-IAS - 6/17: "Bus Protection and Current Transformer Theory" - Current theory leads to better understanding of bus faults and bus protection methods ... [more]

SCV-CAS - 6/21: "Signal Integrity: Design, Test and Tolerance" - Signal integrity for nanometer technology and SoC is becoming a major challenge ... [more]

SCV-PSE - 6/22: "Looking Forward, Looking Back" - Profile of the Symposium this summer in Santa Clara and views about where we're going ... [more]

SCV-EMB - 6/23: "The Artificial Synapse Chip: Towards an Electronic Prosthetic Retina" - Progress in developing a neural interface to connect output from a digital camera to individual retinal cells ... [more]

SCV-Rel - 6/23: "Design and Analysis of Accelerated Reliability Tests" - Alternatives for greatly accelerating tests and analyzing data are valuable for estimating product MTBF ... [more]

SCV-EMS - 6/30: "The Dream - and Focusing to Make it Real" & "Making It - How To Go Into Business For Yourself" - bootstrapping a new company in Silicon Valley, and making it with your own business startup ... [more]

From our advertisers: Conferences and Workshops -- coming to the SF Bay Area

July 13-16: **IEEE Int'l Electronics Manufacturing Technology (IEMT) Symposium** San Jose Marriott - in conjunction with SEMICON/West. [more]

The IEEE's IEMT is an international forum on electronic/photonic/MEMS components and systems manufacturing technology, a joint effort of SEMI and the IEEE CPMT Society. It brings to Silicon Valley a unique venue for engineers and scientists with technical papers on research, development, and applications of manufacturing technology for components, assemblies, and systems. Individual sessions will deal with reliability, green manufacturing, MEMS packaging, design for manufacturing, the effects of using low k materials, wafer level and stacked die packaging, and testing.

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# Bay Area Jobs

The Santa Clara Valley Section now

You will learn:

- Practical solutions to current production problems
- How to meet the process challenges of the 90nm meter node
- The latest trends in final manufacturing technology from recognized experts
- What is new in advanced packaging, reliability, materials, and design for test

Who should attend:

- Process, equipment and materials engineers in assembly, packaging and test
- Process and product development managers

See also the six Professional Development Courses (outlined below).

lists a number of current "open positions" that might be of interest to unemployed IEEE Members. <u>Visit the Section website</u> and see if one of these is for you. If you know of jobs that can be posted for IEEE Members, please contact SCV Section Webmaster Linda Fong.

#### From our advertisers: Tutorials, Short Courses, and Professional Development Classes

Morning Classes at IEMT Symposium in San Jose:
July 13 (IEEE) Introduction to Flip Chip Technology — A User's
Guide - a detailed look at applications, design, reliability,
infrastructure, materials and processing ... [more]

July 13 (IEEE) Materials, Processes and Defect Recognition for Hybrids, Microcircuits and RF/MMIC Modules - understanding defects in relationship to the process, and in conjunction with customer expectations and end-use environment ... [more]

July 13 (IEEE) Advanced Packaging Technology Solutions for Today's Leading Edge Microelectronics - solutions for next-generation microelectronics and where packaging and interconnection are are going ... [more]

Afternoon Classes at IEMT Symposium in San Jose:
July 13 (IEEE) Flip Chip Failure Mode Analysis for Package and
Board Assemblies - reliability test procedures and common failure
modes in advanced area-array packages and board assemblies ...
[more]

July 13 (IEEE) Lead-Free Soldering — Metallurgical Fundamentals, Reflow Applications, and Challenges - the fundamental design considerations of lead-free alloys, with emphasis on metallurgical properties analysis - includes textbook ... [more]

July 13 (IEEE) **Nano - The Next Technology** - an overview of potential applications and some of the complexities of developing nano-technology products ... [more]

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