



Professional Skills & SJSU Short Courses

Photonics West and Microlithography Symp.

Embedded Systems Conference in SF

Quality Electronic Design Symposium

Subscribe

Download the new January GRID.pdf (500 kB) Select one of our Chapter meetings to learn about new developments and to network with fellow professionals -- choose a course or conference to master a new skill. Check for updated information during the month at our GRID website: www.e-grid.net/

Chapter Meetings for January:

SCV-Mag - 1/18: Moore's Law and Magnetic Recording Areal Density - A Processing Perspective - the growth of magnetic recording density from the perspective of thin film processing ... [more]

SCV-PSES - 1/18: Gateway to a New Thinking in Energy Management - Ultracapacitors - ultracapacitor technology, current applications and future opportunities ... [more]

SCV-Nano - 1/18: Congressman Honda: Government support for Nanotechnology in Silicon Valley - 15th Congressional District rep on the 21st Century Nanotechnology Research and Development Act ... [more]

SCV-CNSV - 1/18: Peering into the Network Industry's Future - the causes for the downturn, dynamics of the network industry, and future possibilities ... [more]

SCV-EMB - 1/19: The Diagnostic Potential of Airway Gas Analysis Using Cavity Ringdown Spectroscopy - description of a novel spectrometer-based health monitoring system ... [more]

SCV-PES/IAS - 1/19: Circuit Breaker Protection - covering a number of topics related to low-voltage circuit breakers ... [more]

SCV-SSC - 1/20: Design Considerations for Low-Power High-Sensitivity Integrated WCDMA Direct-Conversion RF Receivers - the move to higher integration, higher sensitivity, lower die area and lower power ... [more]

OEB-IAS - 1/20: A Technical Perspective on Hybrid Electric Vehicles -- Design, Technology, and Environmental Impact - key technologies behind electric-drive vehicle design: advanced batteries, electric traction, fuel cells, hybrid-electric powertrain, and energy management ... [more]

OEB-Com - 1/22: Wireless Networking for the Home - half-day seminar: installing a wireless home network; wireless security; how to configure and use; product roadmap ... [more]

SCV-CNSV - 1/24: Entrepreneurs SIG - this SIG is part of the Consultants' Network of Silicon Valley, and meets on second and fourth Mondays ... [more]

SCV-CE - 1/25: The Content of Storage - digital storage device requirements for key markets, with specs and a storage hierarchy ... [more]

SCV-REL - 1/26: Developments at the Int'l Symposium for Testing and Failure Analysis (ISTFA) - a panel discussion of selected papers from ISTFA ... [more]

SCV-EMS - 1/26: The Job you Save May Be Your Own and Achieving Multi-Dimension Practical Goals - 'Collegial Leadership' describes distributed, and shared, leadership among colleagues ... practices, process automation, team expertise acquisition that lead to higher productivity ... [more]

SCV-CPMT - 1/27: Long Term Trends in Packaging Technology - The International Packaging Roadmap Update - research needs based on the new 2004 ITRS and NEMI Packaging Roadmaps: cost, thermal performance, electrical performance, size, pin count, reliability, and design ... [more]

Engineers Week Banquet

- Wed. Feb. 23, Westin Hotel, Santa Clara - 5:30 PM - 9:15 PM - Honoring SVEC Hall of Fame Inductees Doug Engelbart, Ken Levy, T.J. Rogers, others - Awarding of scholarships; Discover-E presentation - Reduced price through February 4th - Come join the celebration -- bring a team! See Registration Form for more details Organized by Silicon Valley Engineering Council

SCV-Com - 2/9: Open Wireless Architecture (OWA) for Next Generation Wireless and Mobile Communications - integration of different evolving and emerging wireless access technologies in a common flexible and expandable platform ... [more]

SCV-Mag - 2/15 Spin Torque, and Nanorings - description of two new topics in magnetic nanostructures from inception to realization to potential applications ... [more]

SCV-SSC - 2/17: SiGe Heterojunction Bipolar Technology and Applications - HBTs can be easily integrated into a standard CMOS flow for full-fledged manufacturing ... [more]

OEB-IAS - 2/17: Short Circuit and Coordination Analysis Studies - helpful guidelines and advice on producing and then analyzing the computed and graphical results ... [more]

The Council Marketplace

Do you require someone with special skills -- RF, reliability, project planning, communications, design, patent consultations? ... PCB layout, thermal measurements, SCSI, SANs? See our Council Marketplace page. Thank you for supporting our advertisers.

IEEE/CPMT/EMS Short Courses

Professional Skills Development and Management Classes

Stay ahead of changing professional demands; sharpen your competitive edge with up-to-date skills.

Breakthrough Project Management

Date/Time: Wednesday-Thursday, Jan. 19-20, 8:30AM-4:30PM

Location: Verisign, Mountain View

Fee: \$575 for IEEE Members; \$625 non-members

This 2-day course provides participants with a common methodology, terminology and tools that produce more efficient results and increased buy-in through improved visibility, reliability and consistency.

Full course description and registration details.

Memory Power -- Practical Improvements

Date/Time: Thursday half-day, Feb. 3, 8:30AM-Noon

Location: Carl Zeiss Meditec, Dublin

Fee: \$275 for IEEE Members; \$300 non-members

learn how to easily remember names, numbers, events, faces, and facts; packed with practical ideas

Full course description and registration details.

Clear Business & Technical Writing

Date/Time: Tuesday, Feb. 15, 8:30AM-4:30PM

Location: Carl Zeiss Meditec, Dublin

Fee: \$375 for IEEE Members; \$425 non-members

step-by-step process for designing and writing a clear technical document; gain confidence in writing and editing skills

Full course description and registration details.

Presentation Skills For Engineers

Date/Time: Friday, Feb. 18, 8:30AM-4:30PM

Location: VeriSign, Mountain View

Fee: \$425 for IEEE Members; \$475 non-members

step-by-step process for designing and writing a clear technical document; gain confidence in writing and editing skills

Full course description and registration details.

new! Writing Effective E-mail Messages

Date/Time: Thursday, Feb. 24, 8:30AM-4:30PM

SJSU Department of Electrical Engineering:

Winter Short Courses On the SJSU campus (easy parking)

All classes start on January 18th! Register now.

Digital Signal Processing -- 4-day class with labs:

DSP System Design and Implementation [more]

Date/Time: January 18-21, 8:30AM-4:30PM

Instructor: Professor Avtar Singh, Electrical Engineering, SJSU

Digital System Design -- 4-day class with labs:

FPGA DSP System Design [more]

Date/Time: January 18-21, 8:30AM-4:30PM

Instructor: Professor Chen Choo, Electrical Engineering, SJSU

Digital Design -- 4-day class with labs:

VHDL for Synthesis and Verification [more]

Date/Time: January 18-21, 8:30AM-4:30PM

Instructor: Professor Tri Caohuu, Electrical Engineering, SJSU

Microelectronic Device & Technology -- 4-day class:

Device Physics and VLSI Technology [more]

Date/Time: January 18-21, 8:30AM-4:30PM

Instructor: Professor Lili He, Electrical Engineering, SJSU

Networking Engineering -- 3-day class:

Sensor Network Technology [more]

Date/Time: January 18-21, 8:30AM-4:30PM

Instructor: Professor Nader F. Mir, Electrical Engineering, SJSU

For full class outlines, prerequisites, fees, and lecturer biographies, see the Course Flyer:

www.e-grid.net/docs/sjsu.pdf

Location: Exar, Fremont  
Fee: \$375 for IEEE Members; \$425 non-members  
step-by-step process for designing and writing a clear technical document; gain confidence in writing and editing skills  
Full [course description and registration details](#).

*Circulate within your department.*

### Photonics West [\[more\]](#)

- 22-27 January 2005
- San Jose Convention Center
- Conferences -- Courses -- Exhibits

-- **Biomedical Optics** -- **Optoelectronics**  
-- **Lasers and Applications** -- **MOEMS-MEMS**

Coming to the **SF Bay Area**, Photonics West is North America's largest event focused on optics, lasers, biomedical optics, optoelectronic components, and imaging technologies. This year there are 68 Short Courses, over 70 complete conference tracks, and 800 exhibitors.

No-cost admission to the exhibits and keynotes. For Symposium and registration information, please [access the Photonics West website](#) to download the printed Advance Program and registration forms.

### Microlithography Symposium 2005 [\[more\]](#)

- 27 February - 4 March 2005
- San Jose Convention Center

#### Six Conference Tracks:

- Emerging Lithographic Technologies - Metrology, Inspection, and Process Control - Advances in Resist Technology & Processing - Optical Microlithography - Data Analysis & Modeling for Process Control - Design and Process Integration for Microelectronics Manufacturing

#### Sunday half-day courses (25 choices)

- Students Save 50% on Courses; Group discounts available

#### Admission to the Exhibits -- no charge:

- IC design, fabrication, processing, manufacturing

Register by **February 15th** to get the \$100 discount  
Download the [Advance Program and registration form](#)

*The Largest System Design Event in North America*

### EMBEDDED SYSTEMS CONFERENCE [\[more\]](#)

- March 6 - 10, 2005
- Moscone Convention Center, SF

The Embedded Systems Conference offers an exciting line-up of special events. All are no-cost to registered attendees and range from keynote addresses and networking opportunities, to receptions and panel discussions, all designed to enrich your event experience.

**Sunday and Monday full-day tutorials:** - User Interface Design - Embedded Linux Jumpstart - Embedded C Programming - Real-Time UML - Real-Time Kernels - Managing Embedded Projects - Embedded GNU Jumpstart - TCP/IP Networking and 7 more, plus **132 three-hour and 90-minute Technical Classes** on Tuesday through Friday  
**Flexible Registration Packages:**

- 1-day, 2-day, 3-day, full 5-day, or the ePass value
- No-cost Exhibits Pass (includes Keynotes and Panels)

Register by **February 8th** for discount rates.

[Get complete information.](#)

*Ride BART or MUNI to the Powell Street station*

*"Design for Quality in the Era of Uncertainty"*

### International Symposium on Quality Electronic Design [\[more\]](#)

- EDS - CAS - CPMT - SigDA
- March 21-23, 2005
- Double Tree Hotel, San Jose

ISQED is the pioneer and leading conference dealing with CAD and design for manufacturability and quality issues, front to back.

**TUTORIALS** (all day Monday): - Design of sub-90nm CMOS Circuits and Design Methodologies - Package-Chip Co-Design

**Technical Sessions:** - Leakage and Dynamic Power Issues - Test Application and Cost Reduction - Performance and Reliability Analysis for Yield Optimization - Functional Verification and Test Generation - DFM for Circuit Design - Analog Test and BIST- Variability Issues in Nanoscale Circuits - Advances in Floor Planning - Robust Design under Parameter Variations (and more)

Register by **March 14th** for discount rates.

[Get complete information.](#)

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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Maintain your current DList records at our IEEE LISTSERV website: <http://listserv.ieee.org/>

You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address, plus that of the DLIST: [e-GRID@listserv.ieee.org](mailto:e-GRID@listserv.ieee.org).

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director