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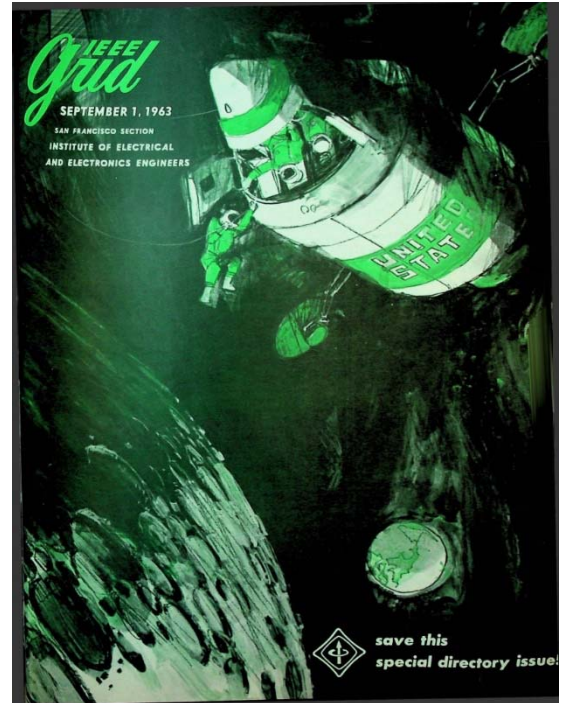
*from a historical perspective ...*

with Paul Wesling, SF Bay Area Council GRID editor (2004-2014)

September, 1963:

Cover: An artist's rendition of the Apollo spacecraft approaching the Moon.

Page 16: IRE member Bernard S (Bernie) Siegal moves to the SF Bay Area, to begin work at Varian Associates. He founds Thermal Engineering Associates in Mountain View, starts the IEEE SEMI-THERM Symposium, and remains a member of my EPS chapter AdCom. See photo below.



2021 EPS Chapter volunteers: Clockwise from front: Wendham Beyene, Gnyan Ramakrishnan, Ravi Agarwal, **Bernie Siegal**, Annette Teng (chair), Gail and Paul Wesling, Tiwei Wei, Chandan Bhat, Azmat Malik, Ed Stoneham, Sandy Chew

Archive of available SF Bay Area GRID Magazines is at this location:

[https://ethw.org/IEEE\\_San\\_Francisco\\_Bay\\_Area\\_Council\\_History](https://ethw.org/IEEE_San_Francisco_Bay_Area_Council_History)

At time of scanning, the bound volumes are held by Paul Wesling. July, 2021 Contact p.wesling@ieee.org



# IEEE *Grid*

SEPTEMBER 1, 1963

SAN FRANCISCO SECTION  
INSTITUTE OF ELECTRICAL  
AND ELECTRONICS ENGINEERS



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special directory issue!



# Microcircuitry...for SMALL Production Runs

For Special Circuits that will NOT be Produced in Great Volume, GI MULTICHIP Technology Can Provide the Ideal, ECONOMICAL Answer

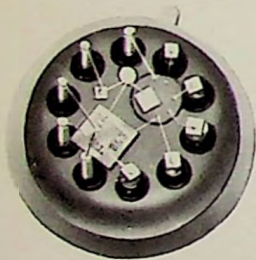
ECONOMICALLY SPEAKING, the familiar "Monolithic" Integral Circuit Package — in which all components are built up on a single, shared substrate — is unsurpassed for circuitry of a fixed, "standardized" character that can be manufactured and sold in vast quantities.

SUCH CIRCUITS as Eccles Jordan Flip-Flops, for instance, are used in 90% of today's computers — and, of course, each computer uses hundreds or thousands of them. The high cost of tooling up to manufacture such a monolith becomes insignificant when divided among the enormous number of units that can be manufactured and sold.

BUT SUPPOSE you want a special circuit of your own design — and suppose, further, that you can use only a few hundred of them... Does this mean that you must abandon the very real advantages of microcircuitry — ? Far from it! That's where General Instrument's highly advanced *multi-chip* technology can give you virtually all the advantages of monoliths (plus a few that even monoliths *don't* offer, in some applications) at a tooling-up cost far below that of a "made-to-order" monolithic ICP.

## What Every Engineer Should Know About GI Multichips

THE INDIVIDUAL components of GI Multichips are batch-manufactured on individual wafers or substrates by exactly the same techniques as are used in monolithic circuits — except that each silicon wafer, instead of forming the base for many *different* components is used as the substrate for a large number of the *same* component — a large number of identical transistors, resistors, diodes, or other circuit elements.



Typical example of multichip circuits:  
General Instrument NAND-Gate on  
TO-5 header

These components are later "diced" apart, and then

assembled to your circuit design.

THE FLEXIBILITY of this process reduces the tooling-up cost to a matter, usually, of only a few hundred dollars... for although the finished circuit combination is custom-made for you alone, the individual components can obviously be combined in other configurations for other customers.

THE NET RESULT — still speaking in terms of economics — is a tooling-up cost that compares favorably with conventional, bulky, discrete components on a circuit board... but with the miniaturization, reliability, rapid switching-time and other outstanding advantages afforded *only* by integral microcircuit techniques. But that's not all...

## Substantial PERFORMANCE Advantages, Too!

IN MANY APPLICATIONS, moreover, the capability of a multichip circuit is definitely superior to that of the best monolith. This is particularly true, for instance, where parasitic *coupling* between components is undesirable or intolerable, such as in high-frequency applications. Such coupling is currently unavoidable in monoliths, because the components share a common substrate — it is easily avoided in a GI multichip. Something quite similar is true where a heat-sensitive component must be isolated from a heat-producing component. Because a GI multichip is composed of *separate* substrate planes, isolation of components from each other is a relatively simple problem.

A FURTHER technical advantage, in some applications, is the fact that the GI Multichip technique permits the selection of substrate materials with physical parameters ideal for *each* component — since only a single type of component will be built on any particular wafer. In the monolith, of course, the substrate must be a *compromise* in its bulk-material properties, since it is shared in common by *many* different kinds of components.

## When It's Hard to Decide — We'll be Glad to Help

NEEDLESS TO say, the GI Multichip is not a panacea. We make monoliths as well, and expect to continue making them for many years to come. Each type has its place; each has certain advantages over the other, depending on the application, the requirements, the specific problem. Our best advice is: when in doubt, please call on us. Our experts will be happy to give you their unbiased, experienced advice.

BUT WE do think the multichip story — all of it — should be better known. We've compiled considerable literature and data on the subject — all of which, of course, is yours for the asking. To avoid delay, write to Jerry Fishel at the address below.

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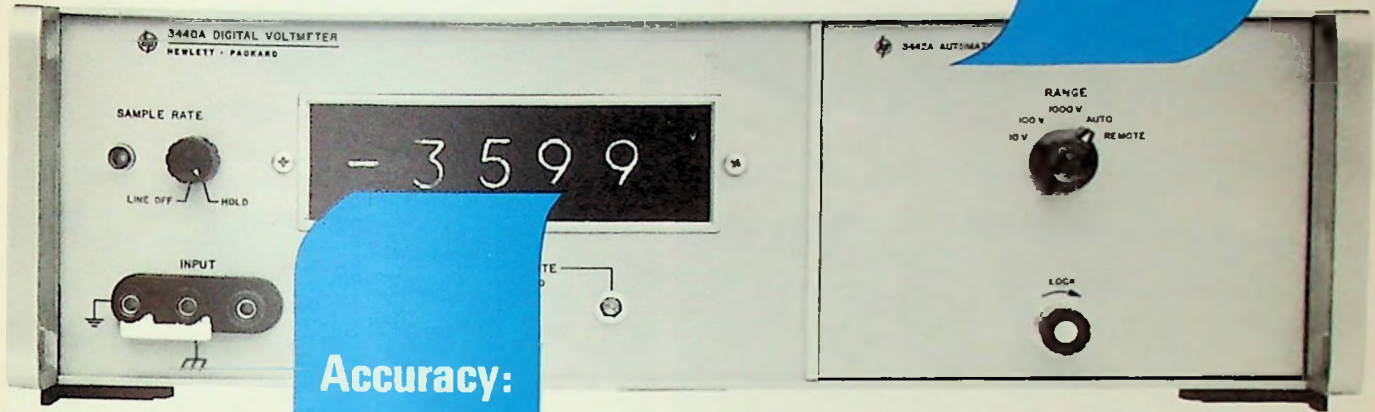




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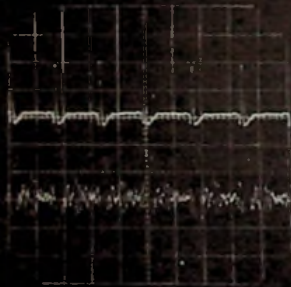
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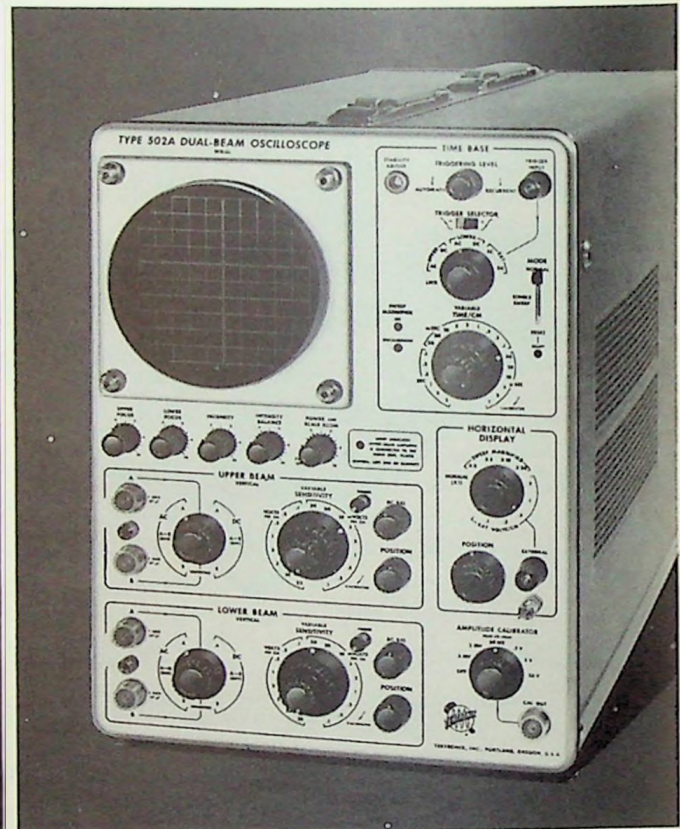
**BIO-MEDICAL APPLICATION.**

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*cover*

Launching the tenth year of **Grid** publication is this special issue containing the directory of officers of the section, the standing committees, the subsections, the technical divisions, and the San Francisco chapters of the professional technical groups. During the first fully merged year ahead, twenty issues will endeavor to keep the mem-

bers informed about the technical program, news of the section and the groups that make it up, and developments in the profession and the industry it serves. The **Grid** is indebted to WEMA, Ed Ferrey, executive vice president, and Larry Bishop, public relations director, for making available the dramatic cover art.

*san francisco section officers*

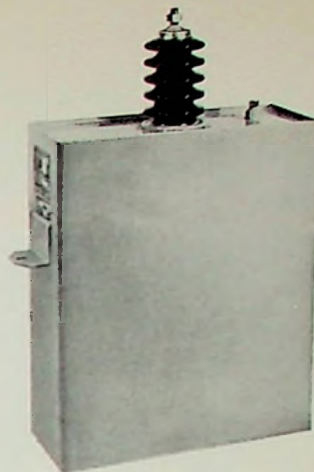
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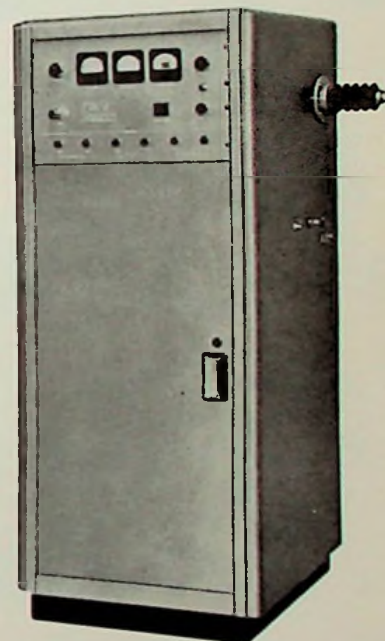
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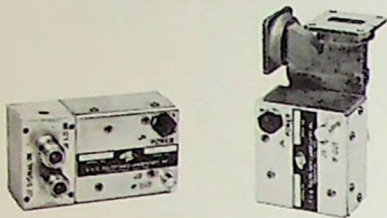
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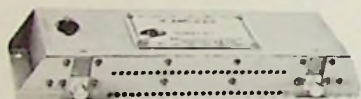
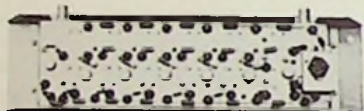


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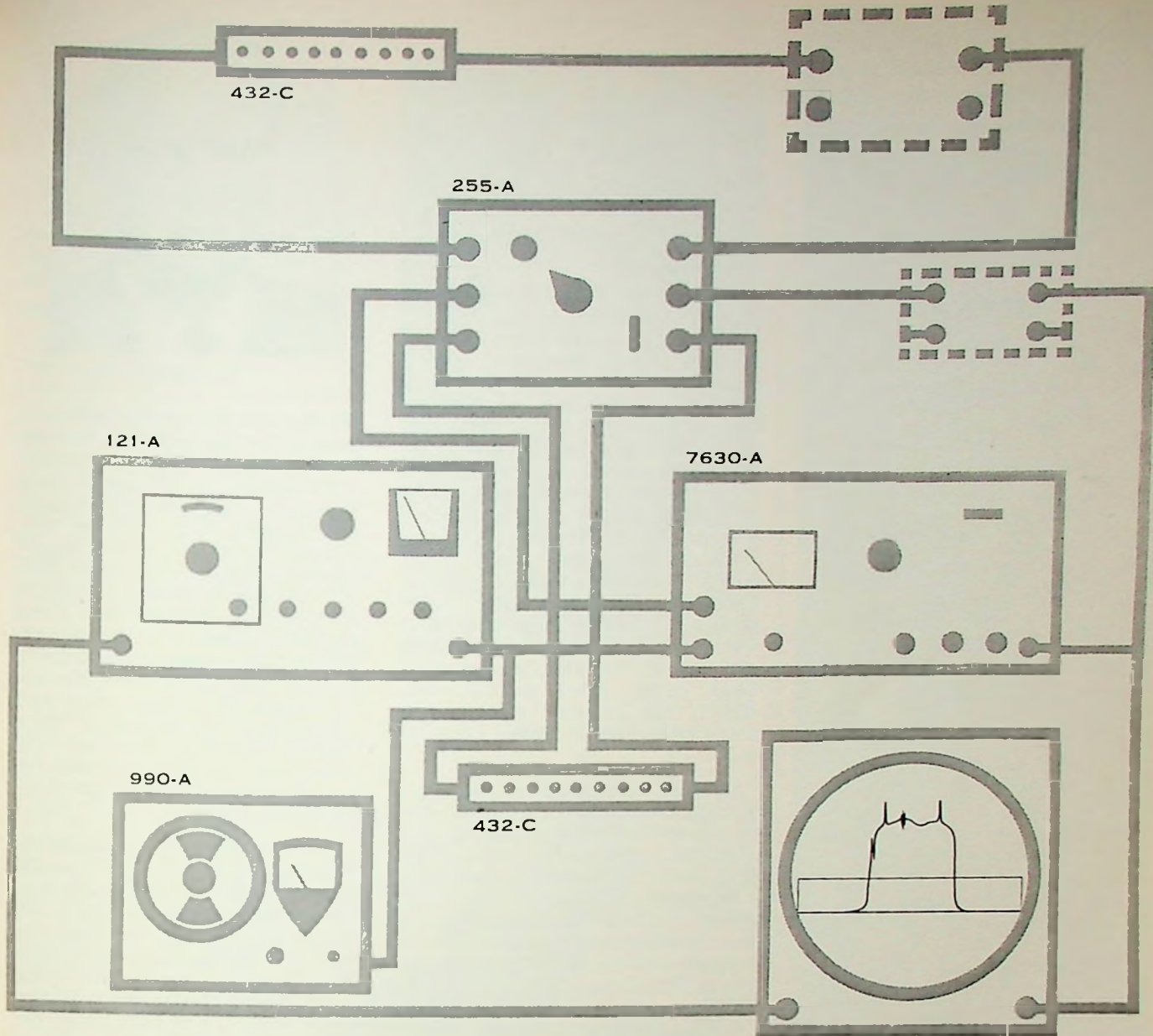
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(Continued on page 10)





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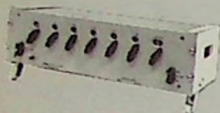


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remarks from the chairs

**REGION 6 MOVES AHEAD**

*Bruce Angwin, long a diligent and outstanding leader in IRE, WESCON, and IEEE affairs, played a key role in the regional meeting in August as chairman of the bylaws and districting committees. He is a nominee for Region 6 Director on the ballot received a short time ago.*



*Bruce Angwin*

The Sixth Region came of age last month in San Francisco. During Wescon, the region's new bylaws were adopted upon which our future operation will be based. In addition, the organizational pattern of the region was formulated and fundamental decisions were made to clarify and initiate programs involving student affairs, awards, and regional meetings. That Sixth Region meeting, therefore, became a major milestone in fusing together the strengths of its parents, the AIEE and the IRE, into a new and virile giant within the IEEE, to better serve its members and their profession.

Under the IEEE a new organizational level, the district, is identified. In contrast to the AIEE's previous "district," this level is optional and falls between the section and region. It offers the opportunity to contiguous sections to support joint activities, such as publications, Wescon, student programs, and PTG activities, and still maintain the many advantages of local fraternal cohesiveness by bringing into critical focus the geographic centers of professional activity that define the ideal section. Also, the district offers much as a solution to the problem of regional committee procedures and regional communications between the 32 sections bounded by Hawaii, the Rockies, Alaska, and Mexico.

The region has, wisely, decided that the districting of sections will be used only where distinct advantage and need exist. Initially, the giant and cumbersome Los Angeles Section has been dissolved and 10 completely independent sections have replaced it. The districting of

these ten sections has been approved to provide the opportunity for Wescon participation, single "Bulletin" publication, business office operation, and PTG chapter participation to the entire membership. Otherwise each section operates with complete autonomy. Other districting opportunities are being explored by a regional planning committee to afford the advantages of long range and consistent planning where rash action may compromise the more solid organizational structure or even confuse communications.

The region has identified Wescon and the IEEE Region 6 Annual Conference as the two technical and organizational meetings of regional scope. Within these activities the broad base of the IEEE both technically and organizationally will be covered. The first conference will convene for the first time in Salt Lake City in April and should prove to be an outstanding event.

As has always been the traditional role of the West, the Sixth Region, IEEE, through its progressive and far-sighted activities, seems destined to lead the institute in its exciting and profitable journey into the future. It now falls on every member to carefully select their representatives at section and region level, demand efficient and effective leadership from them, and wholeheartedly support them in the interesting era before us.

Bruce Angwin



**INFLATABLE ANTENNAS**

*Typical of the reviews of technical meetings submitted by reporters from each of the 19 PTG's active within the San Francisco Section is the following:*

Glen Fisher, supervisor of the microwave application dept., Lockheed Missiles and Space Co., Sunnyvale, addressed the June meeting of PTGAP on the subject, "Inflatable Mechanisms for Space Antennas."

Electronics engineers and physicists seldom give adequate credit to the mechanical engineer who converts their broad specifications into workable structures. Specifically, a variety of large antennas is required for deployment from space vehicles. The lack of gravity, inclement weather, and extraneous torques impose less mechanical strain. However, light weight and compactness are required as well as resistance to the space environment, such as disintegration of materials, wide temperature possibilities, and "cold welding" because of the low pressure.

These problems have not proven



—LMSC photo  
Broadband conical inflatable antenna for use in space

severe by using inflatable antennas of laminated mylar and aluminum foil. Considerable care is required to avoid premature inflation by trapped gas inside the antenna, snarling of the antenna as it inflates, torque forces which would disturb satellite orientation and leakage of undesired gases where sensitive measurements are being made.

Inflating is generally accomplished with a small bottle of carbon dioxide. After the antenna pressure equals the bottle pressure the antenna pressure is relieved as completely as possible by opening valves directly to the ambient vacuum. Seldom does the internal vacuum approach within two orders of magnitude of the surrounding medium, even on extended satellite flights due to the slow molecular flow behavior of the remaining gas.

Many various antenna types have been constructed, tested, and proven in satellite operation. It would appear that any conceivable antenna could now be constructed for space missions in this manner with assurance of success.

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## MEETING CALENDAR

### PROFESSIONAL TECHNICAL GROUPS

#### Electronic Computers

8:15 P.M. • Tuesday, September 17, 1963

(Joint meeting with PTCSET, see below)

#### Space Electronics & Telemetry

8:15 P.M. • Tuesday, September 17, 1963

(Joint meeting with PTGEC)

"Vehicle-Borne Telemetry Data Compression"

Speakers: Richard Schomburg and Harvey Massey

Lockheed Missiles & Space Company, Sunnyvale

Place: Lockheed Auditorium, Bldg. 202, 3251 Hanover St., Palo Alto

Dinner: 6:15 P.M., El Camino Bowl, 2025 El Camino Real, Mountain View

Reservations: Robert Light, 968-6211, Ext. 2024

#### MORE DIRECTORY

#### STUDENT BRANCHES

Heald Engineering College,  
1215 Van Ness Ave.,  
San Francisco, OR 3-5500  
Advisor: Roy O. Hurd

San Jose State College,  
San Jose 14, 294-6414  
Advisor: Harry Engwicht

Stanford University,  
Stanford, 321-3300  
Advisor: Robert W. Newcomb

San Francisco State College,  
1600 Holloway Ave.,  
San Francisco, JU 4-2300  
Advisor: Rene B. Marxheimer

University of California,  
Berkeley 4, TH 5-6000  
Advisor: William J. Welch

University of Santa Clara,  
Santa Clara, AX 6-3360  
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LETTER TO THE EDITOR

Palo Alto

Editor, the Grid:

There are two very fine candidates up for election as Director of the Sixth Region of the IEEE. They are Bruce Angwin and Robert W. Illman.

The purpose of this letter is to say something about Bruce Angwin's unique qualifications for this job at this time, qualifications which cannot be inferred from a simple review of his biography.

In 1955, when I was chairman of the San Francisco Section of the IRE, Bruce Angwin was chairman of the Los Angeles Section. Prior to that time, the relations between the two groups left much to be desired. By a simple handshake with Bruce, all differences between the two sections were resolved, and in the succeeding eight years his continuing participation in IRE affairs in Los Angeles has been a most significant factor in maintaining amiable relations between the sections.

Since 1955, I have served with Bruce on the Seventh Region IRE committee, on the Wescon board, and have consulted with him many times on IRE and IEEE matters. I know of no person who has been more dedicated to the welfare of the IRE and

IEEE, who has spent more time, and who has been more effective than Bruce Angwin. For this first year of the new joint society, Bruce's experience, dedication, and personality make him, in my opinion, the ideal man to serve all of us as Director of the Sixth Region of the IEEE.

ALBERT J. MORRIS  
PRESIDENT  
RADIATION AT STANFORD

section notes

REGULAR TUESDAY LUNCHEON

A special luncheon table is reserved every Tuesday at the San Francisco Engineers Club for members of IEEE. Club membership is not required and a cash ticket may be purchased from the cashier for \$2.00, including tax. No reservations are required.

IEEE members are invited to drop in for lunch whenever they are in the San Francisco area on Tuesdays. The club occupies the 15th floor at 206 Sansome St., San Francisco.

lecture series notes

INFORMATION/COMMAND-CONTROL

A statewide series of 15 lectures on information sciences in command and control will be presented by University of California Extension at Moffett Field, Los Angeles, Fullerton, and San Diego beginning September 23.

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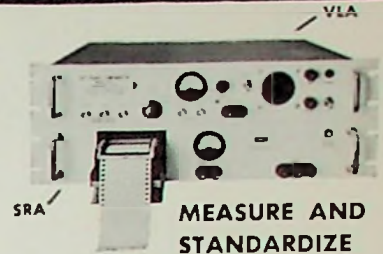
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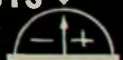
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**IT IS REPORTED:**

Dr. Julius S. Bendat has announced formation of Measurement Analysis Corp., Los Angeles, to conduct research in fields involving mathematical and engineering analysis of physical problems, particularly those requiring measurement and analysis of random phenomena.

William E. Evans, Jr., has been named head of a new data systems dept. at Granger Associates, Palo Alto, reporting to E. W. Pappenfus, vice president for engineering, and dealing broadly in the electronic signal processing and display field. Evans has been manager of A. B. Dick Company's laboratory in Palo Alto since 1959.

Dr. Alan L. Helgesson is a new engineering specialist with Melabs, Palo Alto, and will be engaged in research and development on parametric amplifier devices in the semiconductor devices branch of the microwave dept. to further advance the company's capabilities in the parametric amplifier field.

Robert van Hees has been promoted to the newly created post of director of engineering, and Richard Elsworth has been named chief engineer at Kelvin Electric Co., according to W. I. Elliott, president of the Van Nuys, Calif., manufacturer of precision wire-wound resistors and networks.

Raymond J. Senger, president of Develop - Amatic Engineering, Palo Alto, has announced the appointments of George R. Sommers, as senior vice president-marketing, and Leonard F. Beckers, as senior vice president-finance. They have also been elected directors and members of the executive committee.

Kenneth T. Larkin, associated with the Lockheed Missiles & Space Company since 1956, has been named director of engineering, filling the vacancy created by the death of F. J. Bednarz. Dr. H. Potter Kerfott will serve as assistant director of engineering, and Dr. Roger E. Gaumer as acting laboratory director of the division's mechanical and mathematical sciences laboratory.

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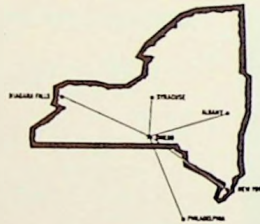
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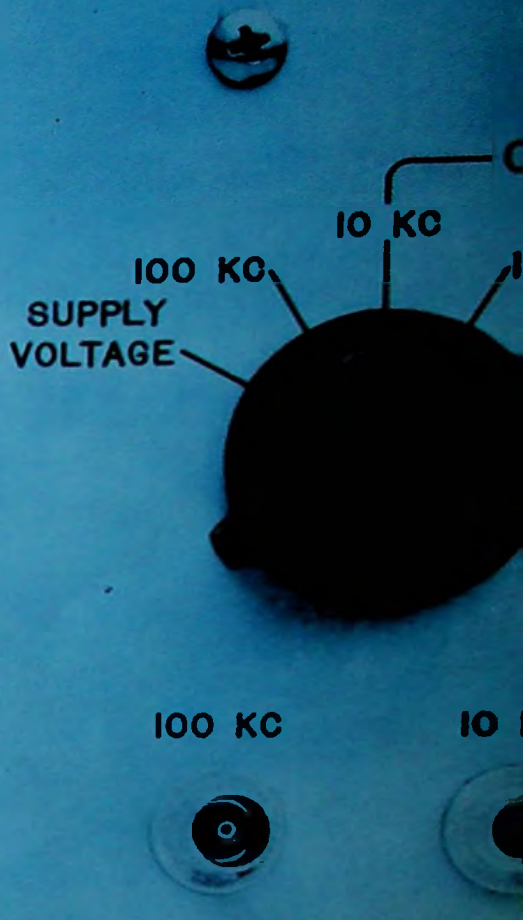
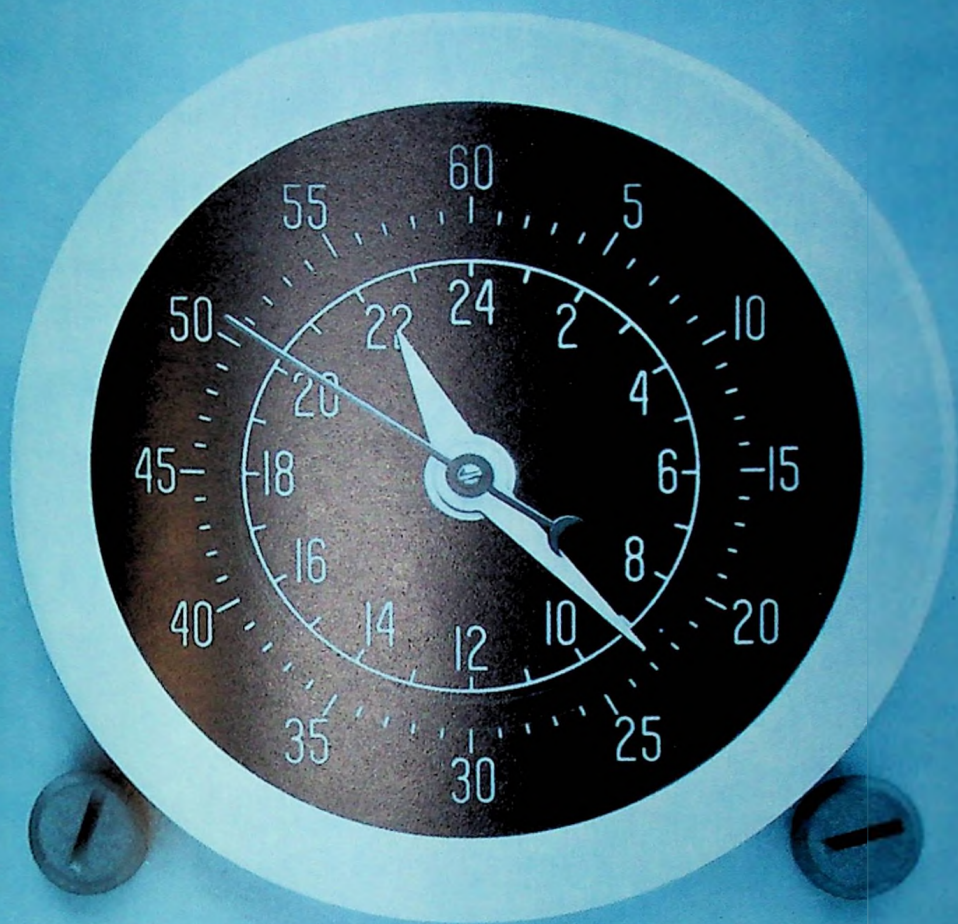
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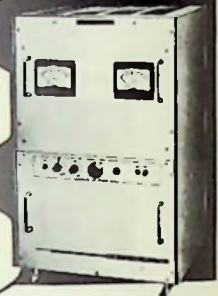
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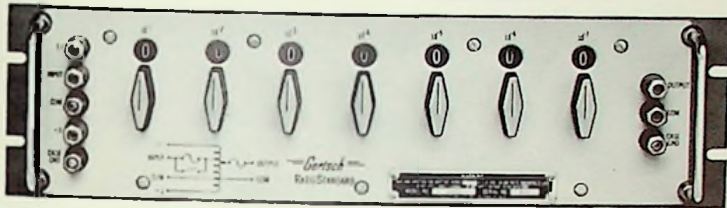
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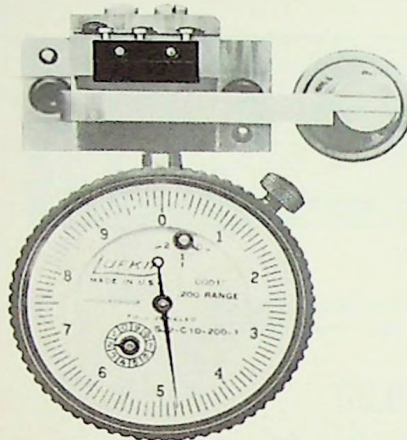


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