Four exemplary patents out of approximately 1000 granted:

US4884198A Single cycle processor/cache interface 12/18/1986

RISC processing unit which selectively
US5159680A isolates register windows by indicating usage 10/25/1991
of adjacent register windows in status register

US5734874A Central processing unit with integrated graphics functions 4/29/1994

US7209996B2 Multi-core multi-thread processor 10/16/2002