IEEE

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San Francisco Bay Area Council   **www.e-grid.net**

| Networking Opportunities at local Chapter Meetings, Tutorials, Workshops, PACE, Consultants Network Conferences, Career Enhancement   Please *Post and Circulate* within your Company/Lab | December 13, 2005 |

Serving the San Francisco, Oakland-East Bay, and Santa Clara Valley Sections of the IEEE
For an online version of this Council announcement, with active links, visit www.e-grid.net/announcements/grid0512b.html

Download the **December GRID.pdf** (800 kB) -- details on upcoming Chapter meetings, classes, and conferences on a wide variety of topics.   Stay ahead of changing professional demands; sharpen your competitive edge with up-to-date technical skills and through your networking activities.   See our **QuickRef Calendar** for a more detailed listing of Chapter meetings.

Chapter Meetings for December, January:

SCV-EDS - 12/13 | **LEDs for Solid State Lighting and Other Emerging Applications: Status, Trends and Challenges** - LED technology, current applications, and trends ... [more]

SCV-AP - 12/13 | **Radiation of Impulse-Like Waveforms With Illustrative Applications** - radiated electric field spectrum fairly flat over two decades of frequencies ... [more]

SCV-CNSV - 12/13 | **Taxes ... What the Rich Do Differently** - legal strategies that may apply to you ... [more]

SCV-Comm - 12/14 | **802.16e: A Mobile Broadband Wireless Standard** - techniques for high speed mobility using various subhchannelization schemes ... [more]

SCV-CPMT- 12/14 | **The Future of Printed Circuit Boards** - being replaced by silicon chips communicating through LEDs or radio beams ... [more]

SF-OEB-SCV- | **Happy Holidays!** - get ready for more networking and learning opportunities during 2006!

SCV-CPMT&LEOS - 1/11 | **Enabling Technologies for Board-Level Optical Interconnects** - integrated polymer waveguides, waveguiding elements, 3D optical wiring schemes ... [more]

SCV-CPMT - 1/17 | **Semiconductor Equipment and Materials Outlook** - market forces affecting packaging, assembly, shipments, with predictions ... [more]

SCV-Mag - 1/17 | **Beyond the Limits of Magnetic Recording: an Itinerant Magnetician Looks at Hysterical Loops** - will we reach the ultimate limit of areal density? ... [more]

SF-PES - 1/18 | **Broadband over Power Lines - FCC Emissions Compliance Guidelines** - with fixes available, there is no excuse for BPL radio interference except ignorance ... [more]

SCV-PES/IAS - 1/18 | **Short Circuit and Coordination Analysis** - computer programs now available produce technically accurate studies ... [more]

SCV-SSC - 1/19 | **Introduction to the Stanford Nanofabrication Facility and Research Examples** - an overview and the various research projects being conducted in the facility ... [more]

SCV-EMB - 1/18 | **Stem Cell Applications in Biomedicine, Tissue Engineering, and Regenerative Medicine** - an overview of stem cell biology, current use in the clinic, and ongoing developments ... [more]

SCV-CE - 1/24 | **CES Debrief** - notable new products and technology from the 2006 Consumer Electronics Show ... [more]

SCV-CPMT- 2/8 | **MultiLayer Ceramic Capacitor (MLCC) Value Drift (and) Embedded Passives** - base-metal-electrode capacitors degrade at 500 hours; embedding capacitors in substrates ... [more]

SCV-EMB - 2/15 | **Biometrics: A Brief History and Review of Current Programs** - recent algorithmic approaches to face and iris recognition ... [more]

SCV-CPMT- 3/8 | **Restriction of Hazardous Substances (RoHS) Directive Implementation Challenges** - conversion to lead-free is much more involved for small and medium-sized companies ... [more]

Note to All IEEE Members
The Council Marketplace
Do you require someone with special skills -- DSP design, RF, reliability, test design, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our **Council Marketplace** page. Thank you for supporting our advertisers.

DesignCon 2006 --
Connecting the World of Electronic Design [more]
-- Conference February 6-9, 2006
-- Santa Clara Convention Center
-- Exhibits February 7-8

Developed specifically for semiconductor and electronic design engineers, **DesignCon** is the essential design engineering event addressing the challenges facing these communities and providing the solutions attendees can implement immediately in their designs. **Find your solution at DesignCon 2006.**

Technical Tracks: - Chip-Level Functional Design - Chip-Level Physical Design and Verification - Power and Package Co-Design - PCB, Package, and Passive Technologies - Chip and Board Interconnect Design - High-Performance Backplane Interconnect Design - High-Speed Timing, Jitter and Noise - Functional Verification (and more)
Management Forum Panels: - Engineering Education in the United States - Today's DFM: the Supply Chain and its Effect on Designers - The Growing Cost of EDA Tools: When to Build versus Buy - The Business of DFM: Critical Issues and Implications (and more)

For the complete program and to register: **www.designcon.com**
Early-Bird rates through January 15th!
Sign up for an exhibits pass.

Need consulting or outsourcing services?
Check out the **GRID Marketplace**.

Int'l Consumer Electronics Show:
The 2006 Storage Visions Conference [more]
-- January 6 & 7, 2006
-- Las Vegas Convention Center
-- Concurrent with the Consumer Electronics Show
-- Admission to the CES Exhibits

Hear the latest on digital content technology from industry leaders -- those who matter in the entertainment value chain -- and find out how current and coming trends can benefit your company, from the experts at Turner Broadcast, Seachange, Microsoft, Scientific Atlanta, Seagate, Maxtor, GS Magic, Samsung Semiconductor, M-Systems, Agere Systems, Marvell, ST Microelectronics, HP, Atmel, Sonic Solutions, Silicon Image, Toshiba, and many others.
Sessions: - Storage and Content Creation, Editing and Distribution - Home Network Storage - Integration of Storage in CE Devices - Mobile CE Storage Products - Optical Storage - Storage and CE Analyst Session - Financial Analyst and VC Session

For complete information, please see the **Storage Visions 2006** website: **www.storagevisions.com** -- or call Kathleen locally at 408-871-8808, to register for the Conference and CES Show Exhibits.

Also at the CES in Las Vegas, from EE TIMES & CMP:
Great Minds, Great Ideas: the People, the Products, and the Technologies That Will Change Our World [more]
-- **January 5**, 2006 at the Sands Hotel, Las Vegas
-- Concurrent with the Consumer Electronics Show
-- no-cost event
  9:00 AM: Disruptive Innovation   10:30 AM: Game Theory   1:00 PM: New Frontiers   2:30 PM: Innovation Generation   4:00 PM: Digital Media
More details on the **Consumer Electronics Show** website

The IEEE First International Conference on
Collaborative Computing:
Networking, Applications and Worksharing [more]
-- December 19-21, 2005
-- DoubleTree Hotel, San Jose
-- Sessions in two tracks

The premier international forum for discussions among academic and industrial researchers, practitioners, and students interested in collaborative networking,

Object Management Group:
Internationalization and Unicode Conference [more]
-- March 6-8, 2006
-- Hyatt Regency Hotel, Burlingame (SF Airport)
-- Tutorials in three tracks: Monday, March 6
-- Sessions in three tracks: Tuesday-Wednesday, March 7-8

Unicode experts, implementers, clients and vendors are invited to attend this unique conference on the Unicode Standard and internationalization. Exchange ideas with leading experts, find out about the needs of potential clients, or get information about new and existing Unicode-enabled products.

worksharing, and applications.

Conference topics: - Distributed Collaboration in Mobile Computing   - Collaborative Enterprise Applications   - Location Estimation and Routing in Wireless Sensor Networks   - Collaborative Framework in Education and e-Learning   - Reputation and Trust Management   - Collaborative Computing in Overlay Networks - Collaborative Information Flow Systems   - Collaborative Editing and Writing Systems   - Floor Control for Collaborative Applications   - Distributed Collaboration in Peer to Peer Computing   (and more)

For more information and to register: **www.collaboratecom.org**
(At-door registration is available.)

Conference topics: - Unicode 5.0 & ISO/IEC 10646   - Text and data mining   - Security and phishing   - Enterprise software in a global environment   - Language tags and locales: implications for developers   - Global development best practices   - Internationalized Domain Names/Resource Identifiers   - Microsoft Office 12 and Windows Vista   - Globalizing your product: business cases and technical issues   - Unicode for Complex Scripts (Arabic, Tamil)

Early-bird registration rates through February 1 - multi-attendee discounts. For more information and to register: **www.unicodeconference.org/ieee**

Faculty Positions: UC-Santa Cruz Computer Engineering Dept
Assistant and Associate Professor Positions

- Assistant Professor with research interests in the area of **Assistive Technology** including sensory augmentation, human-machine interface, wearable computers, prosthetic devices, and technology for the elderly and the disabled.

- Associate Professor in **Computer System Design**, including VLSI design, FPGA design, VLSI CAD, system-on-a-chip design, and related areas.

The UC-Santa Cruz campus is especially interested in candidates who can contribute to the diversity and excellence of the academic community through research, teaching, and service. Submit CV and other details by Jan. 2, 2006. For additional details, please see the descriptions in **this month's GRID.pdf**.   Electronic applications are preferred: **www.soe.ucsc.edu/jobs/faculty/apply**

RFIC-2006 Call for Papers
IEEE Radio Frequency Integrated Circuits Symposium
[more]
   - Paper Summary Deadline: **January 2**, 2006
   - Location & Dates: San Francisco, June 11-13, 2006

Papers are solicited in these areas:
- Cellular System ICs and Architectures   - Wireless Data System ICs and Architectures   - Wide Band Communication System ICs and Architectures   - Optical System ICs and Architectures   - Small-Signal Circuits   - Large-Signal Circuits   - Frequency Generation Circuits   - RFIC & Device Technology   - RFIC Modeling and CAD

For the latest information, including complete Call for Papers, download the **one-page GRID announcement** or visit **www.rfic2006.org**

Call for Papers -- *IEEE Computer Society*
Symposium on Field-Programmable Custom Computing Machines   **[more]**
   -- April 24-26 2006
   -- Napa Valley Marriott
   -- **Abstracts Due January 13**

FCCM is a forum for presenting new research on reconfigurable computing -- the use of field-programmable technologies for high performance and/or low energy computation. Submissions are solicited on a wide variety of topics related to custom computing, including but not limited to:

- Architecture of reconfigurable computing devices and systems   - Languages, compilation techniques, tools, and environments for programming and run-time support   - Applications of reconfigurable computing in scientific computation, mobile communications, medical image processing, data and communication security, network infrastructure   - Possible forms and system implications of reconfiguration for fault tolerance and avoidance   - Novel use of reconfigurability, including evolvable hardware and adaptive computing

Please download the full **Call for Papers** and add the FCCM dates to your calendar.

SUBSCRIBE yourself to this IEEE Council **e-GRID DList**

IEEE Members in the San Francisco Bay Area automatically receive this Announcement at their IEEE "email address of record."   Members may unsubscribe from (or re-subscribe to) this mailing list at **www.ieee.org/ra/e-notice/sfbac-enotice.html**; be sure to include your IEEE member number.   Members wishing to **also** subscribe an alternate email address (eg, Hotmail, or at work), or who **live outside the Bay Area**, may follow the instructions below.

Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails.   This is a controlled list (ie, no spam) -- get more information.   You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired.   To update the list, send an email message to:
      listserv@listserv.ieee.org

...with the following choices in the BODY (not Subject) of the message:

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Or, to receive a text-only notification email, use the following in the BODY (not Subject) of the message:

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to UNSUBSCRIBE:
      unsubscribe   e-GRID-text

Maintain your current DList records at our IEEE LISTSERV website: **http://listserv.ieee.org/**
You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address (editor@e-grid.net), plus that of the DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See **http://www.e-grid.net/**

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