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[Quality Design CFP](#)

[Fall Processor Forum;](#)
[Failure Analysis; Embedded Systems](#)

[NANOWorld'05;](#)
[Consultants' Seminar](#)

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Download the **new October GRID.pdf** (900 kB) or check the attached one-page PDF "summary" -- details on upcoming Chapter meetings, classes, conferences on a wide variety of topics. Select one of the Chapter events to learn about new developments and to network with fellow professionals. See our [QuickRef Calendar](#) for a more detailed listing of Chapter meetings.

Chapter Meetings for September:

SCV-PSES - 9/27 | **Changes in What We Know about Circuit Protection: Is it a Circuit Breaker or Not?** ... [\[more\]](#)

SF-IAS - 9/27 | **Overview of IEEE Standard 1015-1997 ("Blue Book")** - Applying Low-Voltage Circuit Breakers Used in Industrial and Commercial Power Systems ... [\[more\]](#)

SCV-WIE - 9/27 | **Primer on MEMS and Optical MEMS** - two talks on nanotechnology and MEMS ... [\[more\]](#)

SCV-CE - 9/27 | **Video Compression: Principles, Practice and Standards** - overview, plus current and emerging image and video coding standards ... [\[more\]](#)

SCV-Rel - 9/28 | **Built-In Soft Error Resilience for Robust System Design** - challenges of radiation-induced logic soft errors in flip-flops and combinational logic ... [\[more\]](#)

SCV-EMS - 9/28 | **Who the Engineering Manager Is and Isn't and Managing in the Wireless Sensing Industry** - skills and characteristics for great managers, and "smart dust" ... [\[more\]](#)

SCV-Nano - 9/30 | **Investing in Nanotechnology** - doing nanotech deals, emerging business models, perception of opportunities ... [\[more\]](#)

Chapter Meetings for October:

SF-IAS - 10/1 | **Full-day Seminar: 2004 California Electrical Code** - tailored to the interest of Electrical Facility Engineers and Electrical Design Consultants ... [\[more\]](#)

SF-PES - 10/4 | **CEO of Ca-ISO: The Future of California Energy** - re-examining reliability standards and the adequacy of system infrastructure with the head of Calif ISO ... [\[more\]](#)

SCV-CPMT - 10/12 | **Imprint Patterning: An Alternative Circuit Fabrication Process** - a single cost-effective microreplication operation ... [\[more\]](#)

OEB-IAS - 10/13 | **AC Control Power and Digital Protective Relays: Avoiding the Pitfalls** - issues and techniques for digital relays requiring a separate control power source ... [\[more\]](#)

SCV-MTT - 10/13 | **The CloudSat Experimental Satellite 94 GHz High-Efficiency Radar Antenna** - shaped offset high-efficiency tri-reflector antenna ... [\[more\]](#)

SCV-Mag - 10/18 | **A Process View of Recording Head Technology** - feature sizes down to the 100 nm range in just a decade ... [\[more\]](#)

SCV-EMB - 10/19 | **Neural Basis of Reach Preparation and Communication Prosthetics** - cortically-controlled prosthetic systems based on how the brain prepares and helps execute movements ... [\[more\]](#)

SCV-SSC - 10/20 | **A Single-Chip Quad-Band GSM/GPRS Transceiver in 0.18um Standard CMOS** - fully integrated, with high sensitivity and low phase error ... [\[more\]](#)

Review the attached 1-page PDF!

- Summary of all Chapter meetings, with links
- Upcoming conferences and classes
- Easy to forward to co-workers

SCV-CNSV - 10/22 | **One-Day Consultants' Business Seminar** - low-cost educational seminar about Engineering Consulting as a career ... [\[more\]](#)

SCV-CPMT - 10/27 | **Drop Testing of Components in Portable Applications** - portable devices (cell phones, PDAs) are more likely to be dropped than affected by changes in thermal condition ... [\[more\]](#)

SCV-IM - 11/2 | **Blackfin: A Combined RISC/Signal Processing Architecture for a New Era** - embedded processor for audio, video, and communications applications ... [\[more\]](#)

SCV-CPMT - 11/9 | **Carbon Nanotubes: Enhancing Conductivity of Conductive Plastics** - ... [\[more\]](#)

Note to All IEEE Members The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, test design, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our [Council Marketplace](#) page. Thank you for supporting our advertisers.

Upcoming classes from SVTL:

One- and Two-Day Short Courses ... [\[more\]](#)

- Sept 29: **Reliability and Failure Analysis of Advanced Semiconductor Devices & Products** Expand your expertise to meet the challenges of VLSI/SoC era; Reliability and Failure Analysis of complex semiconductor devices and use of modern CAD and FA tools.
- Oct. 5: **Problem Solving Skills for Engineering and Management Professionals** A structured problem-solving process to improve the effectiveness of the entire organization; the 6 phases of effective problem solving
- Oct. 6: **Design-for-Yield (DFY) in the Advanced Technology Era** how yield and design-for-yield (DFY) are addressed; the methods for defect reduction, classification and sourcing
- Oct. 7: **Value Stream Management (VSM) for Operation Improvement** Record specifics about an operation in clear visual form; identify waste in the system and create an optimal and streamlined flow
- Oct. 11: **Bio-Technology & Electronics Convergence - Trends and Opportunities**
- Oct. 12: **Effective Communication Skills for Engineers and Managers**
- Oct. 25: **Carbon NanoTubes (CNTs) Technology and Applications**
- Oct. 27-28: **Compact CMOS Modeling and TCAD Methodology for Nano-Scale Circuit Simulation**
- Nov. 3-4: **Innovation & Creativity for High Tech Professionals**
- Nov. 11: **Power Management for ICs with Focus on Linear Drop Outs (LDO) Regulators**
- Dec. 1-2: **The Dynamic Leader's Toolbox & Applications For Management in the 21st Century**

All classes 9 AM - 4 PM at Silicon Valley Technical Institute's classrooms at 1762 Technology Dr Suite 227 (near the SJ airport) in San Jose. Find out more about each course, and register, at www.svtii.com/SVTL-calendar.htm.

Need consulting or outsourcing services?
Check out the [GRID Marketplace](#).

CALL FOR PAPERS

7th International Symposium on

QUALITY ELECTRONIC DESIGN (ISQED 2006)

- March 27-29, 2006 (papers due Oct. 14th)

- San Jose

ISQED is the pioneer and leading international conference dealing with the design for manufacturability and quality issues front-to-back. Held next spring in San Jose, this conference is easily accessible to Bay Area authors; we invite you to submit a manuscript plus an abstract for consideration.

Summary of topics: - Design for Manufacturability & Quality - Device/Package Interactions & Co-Design - Design Verification - Design for Testability - Robust Devices, Interconnects, & Circuits - EDA Methodologies, Tools, & IP Cores - Interoperability & Reuse - Physical Design, Methodologies & Tools - Effects of Technology on IC Design, Performance, Reliability, & Yield - System-level Design, Methodologies & Tools

For full information, including web-based submission, see the [ISQED Call for Papers](#). For assistance, contact [Tanay Karnik](#), Intel

Saturday One-day Seminar -- SF Industry Applications Society:

Analysis of the Changes in the 2002 National Electrical Code and the 2005 California Energy Efficiency Regulations [\[more\]](#)

- Saturday, October 1st

- 8:00 AM - 4:00 PM (7:30 AM registration)

- Wyndham Garden Hotel, 5990 Stoneridge Mall Road, Pleasanton

- Fee: \$225 (includes lunch, handouts, CEU credits)

- **Bonus:** Advance registration and payment by **September 22** will include a copy of the IAEI 2002 NEC Analysis or a copy of the 2002 National Electrical Code.

Registration Information [on the SF-IAS Chapter Website](#)

FPF'05 Fall Processor Forum:

The Road to Multicore [\[more\]](#)

-- San Jose DoubleTree Hotel
-- Full-day Seminars: Monday and Thursday, October 24 & 27
-- Keynotes and Sessions: Tuesday - Wednesday, October 25-26

Integrating two or more processor cores on a single chip is the hottest trend in high-performance microprocessor design. And it's not just for PCs and servers -- embedded processors are actually setting the pace, with some new designs boasting more than a hundred cores. FPF 2005 focuses on multicore processors, including new product introductions and other dimensions of multicore processing.

Learn about:

- ARM's **Tiger** microarchitecture - IBM's **CELL** for the Sony **PlayStation 3** - IBM's **PowerPC** in Microsoft's **XBox 360** -- P.A. Semi's first uprocessor by former designers of Alpha and SiByte -- StarCore and TI's sophisticated new **DSP cores** and chips

Early-Bird Rates through September 30th!

See the one-page flyer in this month's [GRID.pdf](#), or visit www.in-stat.com/fpf/05.

31st International

Symposium for Testing and Failure Analysis (ISTFA) [\[more\]](#)

-- San Jose Convention Center
-- Workshops: Sunday-Monday, November 6-7
-- Sessions: Tuesday - Thursday, November 8-10

ISTFA'05 combines tutorials, an exhibition, and user group meetings, plus technical sessions with a limited number of selected, high-quality papers in two tracks. Included this year is a new "Failure Analysis Basics" session with presentations on the FA process and classic case histories -- learn more about this critical field and the tools it uses.

See the one-page flyer in this month's [GRID.pdf](#), or visit www.istfa.org/.

NANOWorld 2005

Nanotechnology in Aerospace and Electronics ... [\[more\]](#)

-- November 4-5, 2005
-- Loyola Marymount Univ (near LAX)
-- Los Angeles
-- Free Parking

NANOWorld presents the vision and directions needed to move nanotechnology out of the laboratory and into the design process. Government funding, venture capital investment and university research linked to industry are growing world-wide. It is not a matter of "if" nanotechnology will impact the aerospace and electronics marketplaces. It is simply a matter of "when". Whether you are involved with design, manufacturing or supply chain management, you WILL be affected by this revolutionary technology. Here is an opportunity to interact with the world's leading authorities on nanotechnology and meet some of the companies that are already launching nanotechnology products and services.

Early-bird Rates through October 21!

-- \$150 for IEEE Members, \$200 for non-Members, \$100 for students
Program and on-line registration on the [NANOWorld website](#)

October 24-27 -- Santa Clara Convention Center

GSPx: Pervasive Signal Processing Conference and Expo [\[more\]](#)

GSPx is dedicated to Embedded Signal Processing as the most significant and pervasive enabler in real-world applications. Decision-making engineers, managers, and executives come together at GSPx to spark ideas, develop new partnerships and make design decisions informed by what they see and learn in sessions and on the tradeshow floor. GSPx has three days of technical papers plus and a full day of technical workshops, in parallel with exhibits.

Application areas: - Aerospace - Audio - Automotive - Biomedical - Consumer Electronics - Digital Filtering - Digital Radio - FPGA-based DSP - Image Processing - Industrial Controls - Multi-Core Processors - Power Management - Synthetic Aperture Radar - Speech Processing & Recognition - Telephony, WiFi & VoIP ... plus others

Keynote Speakers: Dr. Andrew Viterbi (co-founder of QUALCOMM); Dave Mothersole (CTO, Freescale Semi); Dr. Rajiv Laroia (CTO, QUALCOMM's OFDMA division)

Technical Workshops

- "Preparing Software for Multi-Core Processors"
- "Model-Based Design for Video Systems on Embedded DSPs"
- "Introduction to Multi-Standard, Multi-Core System Design"
- "DSP-FPGA Systems using C-based Design & Model-Based Design"
- "Rapid Prototyping with FPGAs"
- "Streamlining waveform portability across FPGA architectures"
- "Introduction to Video Compression"

GRID readers get an additional \$300 discount! Complimentary admission to exhibits

Visit www.e-grid.net/conf/gsp.html for more details and the **GRID** Promotion Codes to enable your discount.

IEEE Consultants' Network of Silicon Valley

Consultants' Business Seminar [\[more\]](#)

- Saturday, October 22, 2005
- 8:30 AM - 4:30 PM
- Low Cost, High Value: \$45 for Members (thru Oct 13)
- Includes Continental Breakfast and Sandwich-Buffer Lunch
- at Keypoint Credit Union, 2805 Bowers Ave, Santa Clara

CBS-2005 is an educational seminar about Engineering Consulting as a career. This event has important information for both new and experienced consultants and covers the basics with topics including Rate Surveys, Best Use of Contract Broker Agencies, Contracts & Negotiations, how to use the internet to get leads, and more. Plus "Weather Reports" about What's Hot and What's Not, the realities of Off-Shoring, and the opportunities for Consultants.

Get [more information](#), or use [CNSV's PayPal system](#) (for credit cards)

Professional Skills Courses in the SF Bay Area

Choose from an array of one-day skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters. Classes in October and November:

Clear Business, Technical, and E-mail Writing [\[more\]](#)

- October 6 at Cypress Semiconductor, San Jose
A step-by-step process for designing and writing a clear engineering document, e-mail message, or report. The training involves writing, revising, and editing exercises; critiquing documents; games; and lecture.

Leadership Skills for Engineers [\[more\]](#)

- October 19 at LSI Logic, Milpitas
Today's manager must be fully aware of his/her style and be able to adjust to fit the needs of others and the demands of the situation, obtain relevant information and share it appropriately, and listen and speak for results. Learn analytical tools and cultivate "street smarts".

Other Courses: See www.effectivelearning.com

Communication & Conflict Management Using MBTI [\[more\]](#)

- October 20 at Cypress Semiconductor, San Jose
The Myers-Briggs Type Inventory (MBTI) is the most widely used instrument for gaining a deeper understanding of self, others and interpersonal relationships, with insights on the four basic "people patterns" that hold the key to leadership styles, effective communication, conflict, team building and productivity.

Memory Power (half-day) [\[more\]](#)

- October 27 at Exar Corporation, Fremont
You'll learn how to easily remember names, numbers, events, faces, and facts; this course is packed with practical ideas that you can use every day of your life.

Finance for Non-Finance Professionals (half-day) [\[more\]](#)

- November 1 at Cypress Semiconductor, San Jose
This course provides non-finance professionals with an understanding of basic financial terms and concepts to enable them to connect their activities and work products with the company's bottom line.

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address (editor@e-grid.net), plus that of the DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director
